AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 10, line 8, and ending at page 10, line 25 with:

According to a fourth aspect of the present invention, there is provided a drive control method for a display panel comprising the steps of: generating a modulation clock which serves as a criterion for determining a pulse width of a modulation signal and has a frequency deviation to spread harmonics spectrum as compared to a virtual source clock of a constant frequency, the frequency deviation being so restricted that, if at least two pixels corresponding to two adjacent scanning wirings are displayed based on arbitrary same luminance data, a difference between a display luminance of one pixel in a specified period and a display luminance of the other pixel in the specified period is less than or equal to a tolerable value determined by the luminance data; generating a modulation signal by modulating at ~~lease~~ least a pulse width based on inputted luminance data in synchronization with the modulation clock; selecting a scanning wiring of the display panel; and supplying the modulation signal to a modulation wiring of the display panel.

Please replace the paragraph beginning at page 10, line 26, and ending at page 11, line 16 with:

According to a fifth aspect of the present invention, there is provided a drive control method for a display panel comprising the steps of: generating a modulation clock which serves as a criterion for determining a pulse width of a modulation signal and has a frequency deviation to spread harmonics spectrum as compared to a virtual source clock of a constant frequency, the frequency deviation being so restricted that, if an arbitrary pixel is displayed based on arbitrary same luminance data, a difference between a display luminance in a specified period obtained by the virtual source clock and a display luminance in the specified period obtained by the modulation clock is less than or equal to a tolerable value determined by the luminance data; generating a modulation signal by modulating at ~~lease~~ least a pulse width based on inputted luminance data in synchronization with the modulation clock; selecting a scanning wiring of the display panel; and supplying the modulation signal to a modulation wiring of the display panel.

Please replace the paragraph beginning at page 11, line 17, and ending at page 12, line 3 with:

According to a sixth aspect of the present invention, there is provided a drive control method for a display panel comprising the steps of: generating a modulation clock which serves as a criterion for determining a pulse width of a modulation signal and has a frequency deviation to spread harmonics spectrum as compared to a virtual source clock of a constant frequency; converting a gradation of luminance data in order to compensate for changes in a display luminance level due to the frequency deviation; generating a modulation signal by

modulating at ~~lease~~ least a pulse width based on inputted luminance data in synchronization with the modulation clock; selecting a scanning wiring of the display panel; and supplying the modulation signal to a modulation wiring of the display panel.